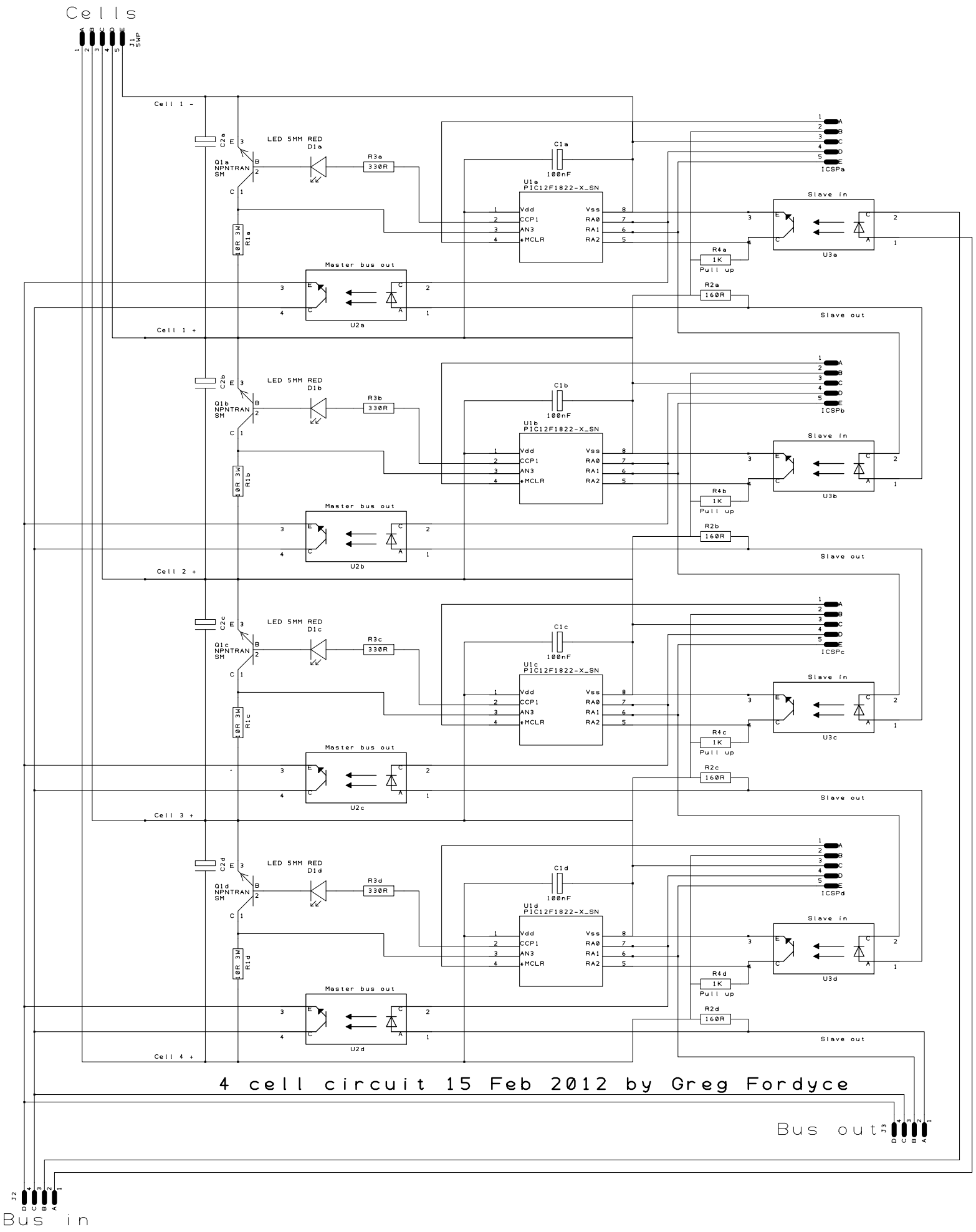
Cells
Cell 1 -
Cell 1 +
Cell 2 +
Cell 3 +
Cell 4 +
C2a
Q1a NPNTRAN SM
LED 5MM RED D1a
R3a 330R
C1a 100nF
U1a PIC12F1822-X_SN
R1a 10R 3W
Master bus out
U2a
ICSPa
Slave in
U3a
R4a 1K Pull up
R2a 160R
Slave out
C2b
Q1b NPNTRAN SM
LED 5MM RED D1b
R3b 330R
C1b 100nF
U1b PIC12F1822-X_SN
R1b 10R 3W
Master bus out
U2b
ICSPb
Slave in
U3b
R4b 1K Pull up
R2b 160R
Slave out
C2c
Q1c NPNTRAN SM
LED 5MM RED D1c
R3c 330R
C1c 100nF
U1c PIC12F1822-X_SN
R1c 10R 3W
Master bus out
U2c
ICSPc
Slave in
U3c
R4c 1K Pull up
R2c 160R
Slave out
C2d
Q1d NPNTRAN SM
LED 5MM RED D1d
R3d 330R
C1d 100nF
U1d PIC12F1822-X_SN
R1d 10R 3W
Master bus out
U2d
ICSPd
Slave in
U3d
R4d 1K Pull up
R2d 160R
Slave out
4 cell circuit 15 Feb 2012 by Greg Fordyce
Bus out
J3
Bus in
J2